RESUME

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OBJECTIVE:

To work in a creative environment where my knowledge can be used to the maximum and where I can constantly learn.

ACADEMIC PROFILE:

Educational Qualification	Board/University	Name of the Institute	Year of Passing	% of Marks
Ph.D	Kakatiya University	KU	pursuing	
M.TECH (VLSI Design & Automation Techniques)	NIT	NIT HAMIRPUR	2012	8.27 (CGPA)
B.TECH (Electronics & Communication Engineering)	JNTU-H	SR Engineering College	2009	72.88%
INTERMEDIATE	Board of Intermediate Education, A.P.	Siddhartha Junior College	2005	95.7%
S.S.C.	Board of Secondary Education, A.P.	Sri Vivekananda High School	2003	80.16%

TECHINICAL SKILLS:

- Tools: Verilog Simulators (Xilinx-ISE 13, Mentor Graphics-Modelsim), T-SPICE, Cadence.
- Concepts : Analog/Digital design

EXPERIENCE:

- Working as **Assistant Professor** in Kakatiya university college of Engineering and Technology in the department of ECE from 2013.
- Working as contract lecturer in Kakatiya university college of Engineering and Technology in the department of ECE in the academic year 2012-13.

TECHNICAL PROJECTS:

- **"**FPGA Based design & Implementation of Viterbi Decoder for a Wi-Fi Receiver.**"**
- **"**Strength Reduced Parallel Chien Search Architecture for Strong BCH Codes.**"**

Research Publications

Journals:

[1]. A. Manoj Kumar and **Mahender Veshala** "Control Home Appliances over IOT", *International Journal of Research (IJR)*, ISSN 2348-6848, Volume 4, Issue 13, pp 230-232, October 2017.

[2]. **Mahender Veshala** and T. Srinivasulu "High Computational Speed with an Optimal Utilization of Resources for Forward Error Correcting Decoder", *International Journal of Emerging Technology and Advanced Engineering*, ISSN 2250-2459, Volume 7, Issue 8, pp 314-318, August 2017.

[3]. G. Vimla Shalini and **Mahender Veshala** "Auto-Configuration and Wisdom Connection system on IOT Applications", *International Journal of Advanced Research Trends in Engineering and Technology*, ISSN 2394-3777, Volume 4, Issue 9, pp 162-168, April 2017.

[4]. P. Rohith and **Mahender Veshala** "Underwater Positioning and Navigation Based on RF Communication", *International Journal of Advanced Research Trends in Engineering and Technology(IJARTET)*, ISSN 2394-3777, Volume 4, Issue 17, pp 149-154, March 2017.

[5]. Sangam Sai Krishna kumar and **Mahender Veshala**, "An Innovative Piecewise Linear Companding Method for PAPR Reduction and Companding Distortion in OFDM System", *International Journal of Computer Engineering in Research Trends*, ISSN 2349-7084, Volume 3, Issue 8, pp 404-410,August 2016.

[6]. N. Navya Teja and **Mahender Veshala**, "A unique Strategy for Swift Generation and Contrast of Applied Feature Vectors", *International Journal of Computer Engineering in Research Trends*, ISSN 2349-7084, Volume 3, Issue 8, pp 431-4435, August 2016.

[7]. **Mahender Veshala**, Ramchander Jatooth and Kota Rajesh Reddy, "Reduction of Short- Channel Effects in FINFET" International Journal of Engineering and Innovative Technology ISSN 2277-3754, Volume 2, Issue 9, pp 118-124, March 2013.

Conference papers:

[1]. Pedada Madhavi, and **Mahender Veshala**, "Light Fidelity Advanced Alternative for Wireless Communications", International conference on Photonics, VLSI and signal processing, pp 585-588, 2014.

[2]. **Mahender Veshala**, and Srinivasulu Tadisetty, "Advanced LED Packages", International conference on Photonics, VLSI and signal processing, , pp 456-461, 2014.

[3]. Muthyala Nakshthra, and **Mahender Veshala**, "Cardiac Disease Recognition Using Timbral Feature Vector", International conference on Photonics, VLSI and signal processing, pp 5256-530, 2014.

[4]. **Mahender Veshala**, Thuasagari Padmaja and Karthik Ghanta, "FPGA Based Design and Implementation of Modified Viterbi Decoder for a Wi-Fi Receiver", IEEE Conference on Information and Communication Technologies, pp 525-529, 2013.

[5]. **Mahender Veshala**,Ramchander Jatooth and Kota Rajesh Reddy, "Diminished Offset Current Analysis in FINFET", IEEE Conference on Microelectronics, Communication and Renewable Energy , 2013.

[6]. **Mahender Veshala**, Balakrishna J and Gagnesh Kumar, "VLSI Based Design and Implementation of Modified Viterbi Algorithm for a Wi-Fi Receiver", International Conference on Recent Trends of Computer Technology in Academia, pp 426-430, 2012.

[7]. Balakrishna Jerripothula, **Mahender Veshala** and Gagnesh Kumar, "Performance Comparison on Various Cryptographic Techniques", International Conference on Recent Trends of Computer Technology in Academia, pp 526-530, 2012.

[8]. **Mahender Veshala**, Balakrishna J and Gagnesh Kumar, "An Optimal Utilization of Resources for Forward Error Correcting Decoder Using BCH Codes", International Conference on Computers and Communication, pp 420-424, 2012.

WORKSHOP:

- Participated a workshop entitled 'iB Hubs POWER TO INDIA' about "Entrepreneurship & Emerging Technologies" in September,2017

- Attended UGC Sponsored 47th Orientation program at JNTU Hyderabad in May/June, 2017.

- Participated faculty Updation program on "Digital VLSI System Design using Verilog HDL", organized by C-DAC, Hyderabad in collaboration with Telangana Academy for Skill and Knowledge (TASK) in January,2016.

- Participated faculty development program on "Research Methodology & Computational Techniques (RMCT-16)", organized by the Department of Mechanical Engineering, KITS Warangal in JUNE/JULY, 2016.

- Participated ISTE workshop on "Analog Electronics", conducted by IIT Kharagpur, Which was held under the National Mission on Education through ICT (MHRD) in June, 2013.

- Participated AICTE Sponsored faculty development program on "VLSI Design", organized by the Department of E.C.E. in association with ISTE chapter of VNRVJIET & EnhanceEDU, IIIT Hyderabad at VNR Vignana Jyothi Institute of Engineering and Technology, Hyderabad in May/June, 2013.

- Participated faculty development program on "Advanced Signal processing Techniques in Communications (ASPTC) 2013", organized by the Department of E.C.E. KITS Warangal in March, 2013.

ACHIEVEMENTS:

- Served as Co- Convener and Faculty Coordinator for Two Day National Workshop on "PCB Design and Fabrication" Organized by Department of E.C. E, KUCET in February, 2018.
- Served as Faculty Coordinator for "Swachh Brarat programme" Organized by KUCET, NSS unit I & II at Kakatiya University in August, 2017.
- Served as Faculty Coordinator for "Defence Electronics" Workshop Organized by Department of E.C. E, KUCET in February, 2014.
- Convener and one of the Editorial member for International Conference on Photonics, VLSI and Signal processing(ICPVS), which was held at KU College of Engineering & Technology in March 2014.
- Qualified UGC-NET (National Eligibility Test) JRF held in December 2011.
- Qualified Gate three times with more than 98 percentile.
- Secured topper of college in Intermediate.

DECLARATION:

I declare that all the information given here are to the best of my knowledge and belief, true and correct. I agree and accept the terms and conditions of this esteemed company.

Date :

Place : (**MAHENDER V**)